



SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION

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Marc Tremblay, et al.FILING DATE
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	"Lock-Based Programs and Transactional Lock-Free Execution", by Ravi Rajwar et al., University of Wisconsin-Madison Technical Report #1440, April 2002.		
	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F Martinez et al., Workshop on Memory Performance Issues, International Symposium on Computer Architecture, June 2001.		
	"Transactional Memory: Architectural Support for Lock-Free Data Structures", by Maurice Herlihy.		
	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F. Martinez et al., Technical Report UIUCDCS-R-2001-2202, February 2001.		
	"Transactional Lock-Free Execution of Lock-Based Programs", by Ravi Rajwar, Proceedings of the Tenth International Conference on Architectural Support for Programming Languages and Operating Systems, Oct. 6-Oct. 9, 2002, San Jose, CA.		
EXAMINER	/Robert Fennema/	DATE CONSIDERED	04/09/2008

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